IN THE CLAIMS:

Please amend claims 1, 6, 13 and 16, and cancel claims 3-5 and 15 as follows below:

1.   (Currently amended) A system for frequency correction in a reception apparatus, the reception apparatus comprising a mobile station for a mobile communication system, the system comprising:

a detection device adapted to detect a frequency discrepancy in received signals;

a first control system configured to correct a frequency supplied to a mixer stage on the basis of the detected frequency discrepancy; and

a second control system configured to provide digital correction of the detected frequency discrepancy in accordance with an algorithm,

wherein only one of the two control systems is active at a time, and

wherein the mobile station is configured such that the first control system is active during a reception mode with one base station, and the second control system is active when a transmission or reception mode is being changed over to another base station.

2.   (Original) The system of Claim 1, wherein the algorithm comprises a CORDIC algorithm.

3.   (Canceled).

4.   (Canceled).

5.   (Canceled).

6.   (Currently amended) ~~The system of claim 1~~ A system for frequency correction in a reception apparatus, comprising:

a detection device adapted to detect a frequency discrepancy in received signals;

a first control system configured to correct a frequency supplied to a mixer stage on the basis of the detected frequency discrepancy; and

a second control system configured to provide digital correction of the detected frequency discrepancy in accordance with an algorithm,

wherein the first control system comprises a PLL control loop having a first voltage-controlled oscillator whose output frequency is supplied to the mixer stage.

7.   (Original) The system of Claim 6, wherein the first control system further comprises a second voltage-controlled oscillator to which a control signal produced based on the detection of the frequency discrepancy is supplied and whose output frequency is an input frequency for the PLL control loop.

8.   (Original) The system of Claim 7, wherein the first control system further comprises a low-pass filter upstream of the second voltage-controlled oscillator.

9.   (Original) The system of Claim 1, wherein the first control system comprises means for detecting the frequency discrepancy as part of a RAKE receiver operable to produce a frequency discrepancy signal at its output.

10.   (Original) The system of Claim 9, wherein the first control system comprises means for producing a control voltage to which the frequency discrepancy signal is supplied and supplying the control voltage to the second voltage-controlled oscillator.

11.   (Original) The system of Claim 1, wherein the second control system comprises a CORDIC computation unit within a reception path that is controlled by a control signal.

12. (Original) The system of Claim 11, wherein the first control system is deactivated at the same time as the control signal is output.

13. (Currently amended) A method for frequency correction in a reception apparatus, the reception apparatus comprising a mobile station for a mobile communication system, comprising:

detecting, in a first operating state, a frequency discrepancy in received signals;

supplying, in the first operating state, a corrected frequency to a mixer stage based on the detected discrepancy, wherein the first operating state comprises a normal transmission or reception mode with one base station;

detecting, in a second operating state, a frequency discrepancy in the received signals, wherein the second operating state comprises a state in which the transmission or reception mode is changed over to another base station; and

performing, in the second operating state, digital frequency correction based on an algorithm.

14. (Original) The method of Claim 13, wherein the algorithm comprises a CORDIC algorithm.

15. (Canceled).

16. (Currently amended) The method of Claim 13 A method for frequency correction in a reception apparatus, comprising:

detecting, in a first operating state, a frequency discrepancy in received signals;

supplying, in the first operating state, a corrected frequency to a mixer stage based on the detected discrepancy;

detecting, in a second operating state, a frequency discrepancy in the received signals; and

_____performing, in the second operating state, digital frequency correction based on an algorithm_, wherein the first operating state involves the detection of the frequency discrepancy being taken as a basis for producing a frequency discrepancy signal, a control voltage derived from the frequency discrepancy signal being supplied to a voltage-controlled oscillator whose output frequency is supplied to a PLL control loop, and the latter's output frequency being supplied to the mixer stage.

17.   (Original) The method of Claim 16, wherein, during the second operating state, the output frequency which is output by the voltage-controlled oscillator at the time of the changeover from the first to the second operating state is maintained.